	Subclass	CLASSIFICATION					PATENT NUMBER
2	Class	ISSUE	U.S. UTI I		Application		
	* * *		SCANNED _	O.I.P.E.	AA PATEN	IT DATE	
Applicating Application	APPLICATION NO. 09/955270 Christopher	CONT/PRIOR D Chevall	CLASS 327 ier	SUBCLASS 392	ART UNIT 28+6 28/5	EXAMINER (a)	
TITLE ADD	Supply volt	age redu	tion cir	cuit for :	integrated	circuit	

		ISSUING C	LASSIFICATION				
ORIGINA	L.		CROSS REFERENCE(S)				
CLASS SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL	CLASSIFICATION						
		·					
			Continued on Issue Slip Inside File Jacket				

TERMINAL	DRAWINGS			CLAIMS ALLOWED		
☐ DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
☐ The term of this patent				NOTICE OF ALLOWANCE MAILED		
subsequent to (date) has been disclaimed.	(Assistant	Examiner)	(Date)			
☐ The term of this patent shall	· - · · ·				•	
not extend beyond the expiration date of U.S Patent. No				ISSUE FEE		
				Amount Due	Date Paid	
	(Primary E	ixaminer)	(Date)	•		
☐ The terminalmonths of				ISSUE BA	TCH NUMBER	
this patent have been disclaimed.	(Legal Instrume	ents Examiner)	(Date)			
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orm PTO-436A Rev. 6/99)			FILED WITH:	DISK (CRF)	FICHE CD-RO	

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